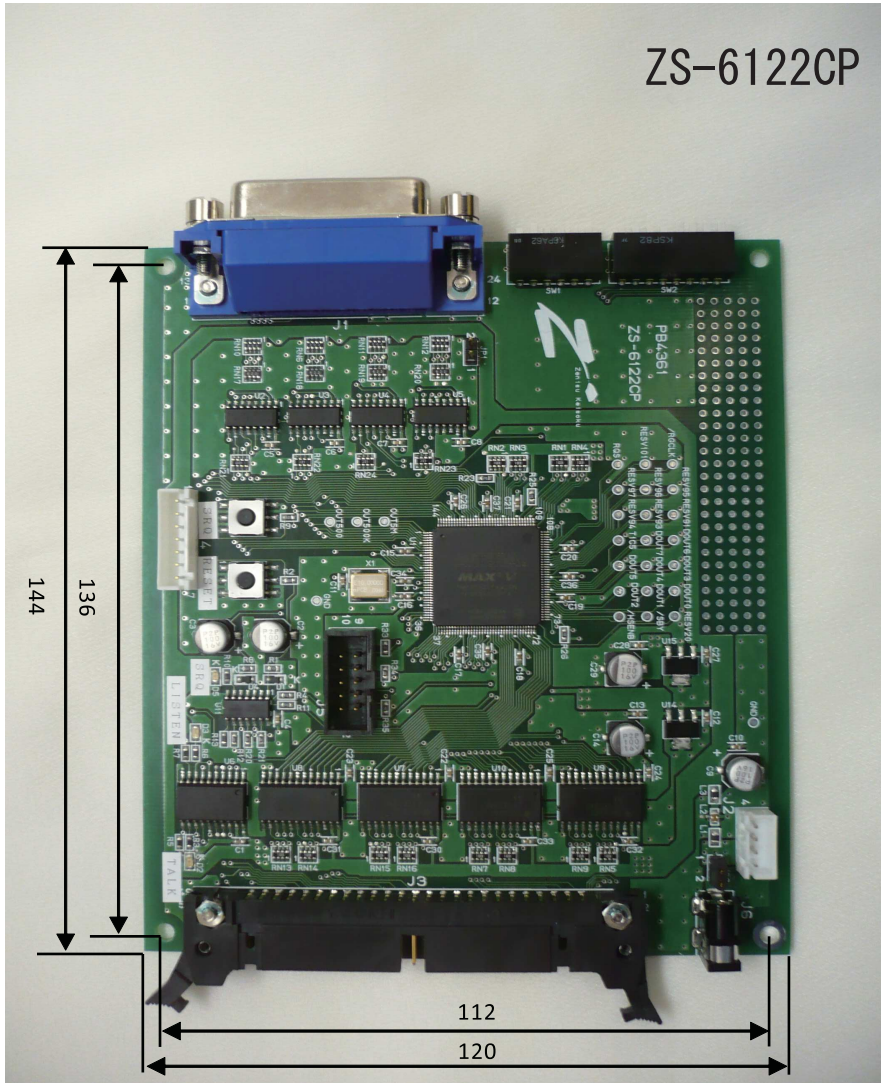


GP-IB Adapter ZS-6122C Series

RoHS 対応

GP-IB and Parallel signal interface



ZS-6122 is a GP-IB adapter that makes it easy to handle parallel signals to communicate with the GP-IB bus.

This unit captures parallel signals such as BCD and binary to the PC, and it is possible to do start and stop, output of relay ON/OFF controlling.

- It is possible to communicate with 4 bytes of input / output data.
- Input / Output assignment can be selected with the DIP switch.
- It is prepared a control signal that can be synchronized with the connected device.



ZS-6122C Series

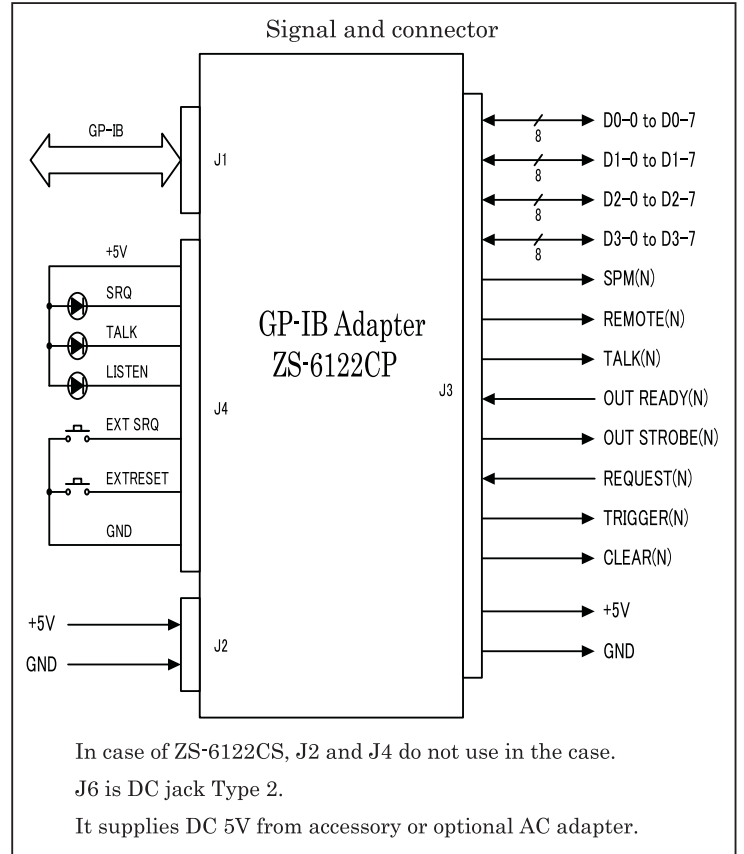
【 Feature 】

- It has both input and output functions.
- It is a small size and low price.
- It is easy to incorporate with equipment.

【 Specifications 】

- Compliant with GP-IB interface standard (IEEE Std 488-1978)
SH1,AH1,T6,L4,SR1,RL1,PP0,DC1,DT1,CO
- It sends input data to GP-IB side in talker operation.
Number of input data : 0 to 4 byte (Delimiter is fixed to EOI)
Signal level : TTL, Fan-in = 1, 10KΩ PULL UP resistor
- It outputs data from GP-IB side in listener operation.
Number of output data : 0 to 4 bytes (Delimiter is optional)
Signal level : TTL, Fan-out = 10

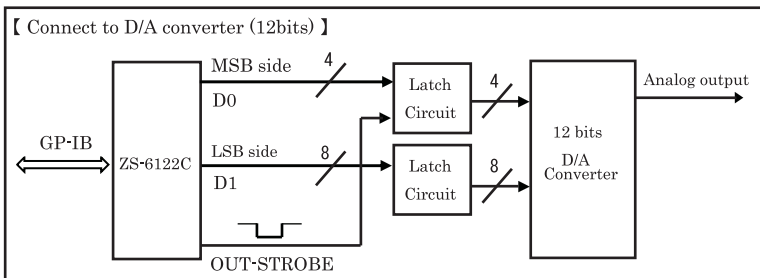
Model	ZS-6122CP	ZS-6122CS
Type	Printed circuit type	Built-in small case
Power	+5V 0.4A	+5V 0.4A
Size	120 x 144 x 16 (H) mm	130 x 150 x 35 (H) mm
Weight	120g	400g
Accessory	J2 Cable 60cm x 1	J6 Cable 60cm x 1
	J4 Cable 60cm x 1	
	J3 Connector x 1 FAS-5001-2101-0BF	J3 Connector x 1 FAS-5001-2101-0BF



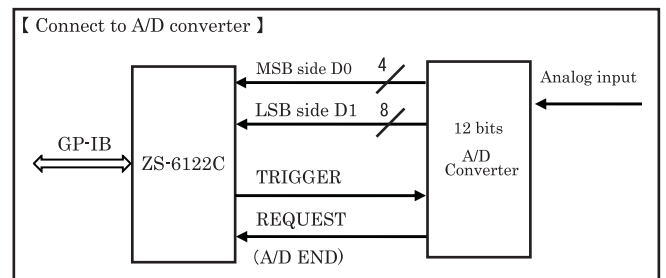
【 Type and operation of the control signal connected to the J3 connector 】

Signal	Signal		Description
	In/Out	Logic	
SPM	OUT	N	It becomes LOW level in serial polling.
REMOTE	OUT	N	It becomes LOW level in remote status.
TALK	OUT	N	It becomes LOW level in talker operation.
OUT-READY	IN	P	It becomes HIGH level when I/O equipment can receive data.
OUT-STROBE	OUT	P	1 μs negative pulse is output after all the bytes of output data are aligned. It is possible to be selected 1ms by DIP switch.
REQUEST	IN	N	SRQ is generated at the falling edge of this signal.
TRIGGER	OUT	N	2 μs negative pulse is output when GET command is received.
CLEAR	OUT	N	2 μs negative pulse is output when SDC command or DCL command is received.

Note) N is Negative Logic, P is Positive Logic



The order of output data is D0, D1 so that latch circuit is required before D/A converter.
It is necessary to add 1 byte dummy data for outputting OUT-STROBE pulse.



It does A/D START with TRIGGER and generate SRQ with A/D END or A/D BUSY termination. However, in case of high-speed operation where A/D operates in several tens μs, there is a better case that a method of reading data immediately after A/D START may be used without using SRQ.



Zenisu Keisoku, Inc.

Zip code : 183-0027

2-13-37, Honmachi, Fuchu, Tokyo, Japan

TEL : +81-(0)42-368-2126 FAX : +81-(0)42-364-0067

Specifications and appearance are subject to change without notice due to continual improvements.