

USB Adapter

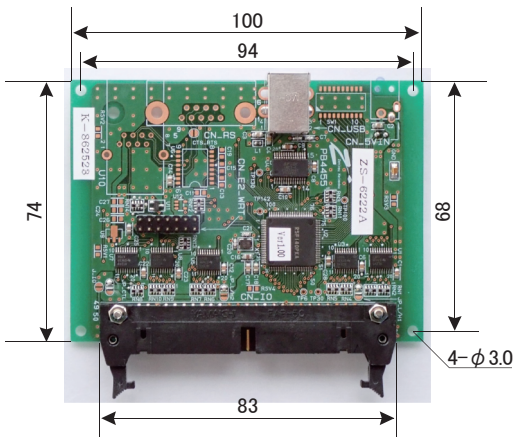
ZS-6222AP / AS

Compliant with RoHS

Allows digital signals to communicate with USB interface Compact, low-cost, easy-to-handle USB to DIO adapter.

It is easy to control digital signals such as BCD, binary, etc., by importing them into a PC. ON/OFF output from the PC can be easily controlled.


ZS-6222AP



Printed-circuit board type is convenient for built-in and prototype experiments.

74x100x20 (H) mm
Approx. 70g

ZS-6222AS



ZS-6222AP is built into a small case.

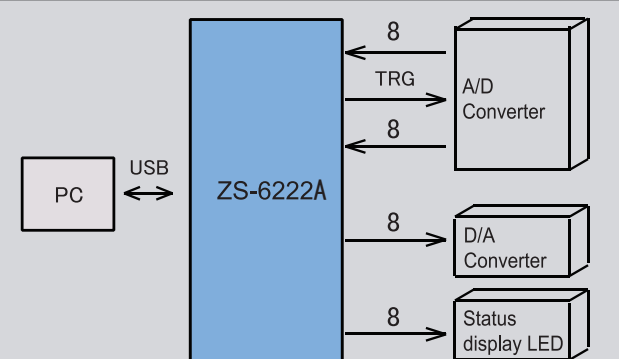
80x110x30 (H) mm
Approx. 180g or less

Feature

- Compliant with USB 2.0
- Power supply +5V from USB.
- Digital I/O 32 bits.
It can be selected I/O with byte unit.
- DIO interface (74AC245) has enough output drive.
- The operation mode is set by command.
- Small and easy to use.
- Value pricing.
- Operation compatible with the former product ZS-6222P/S

Example for usage

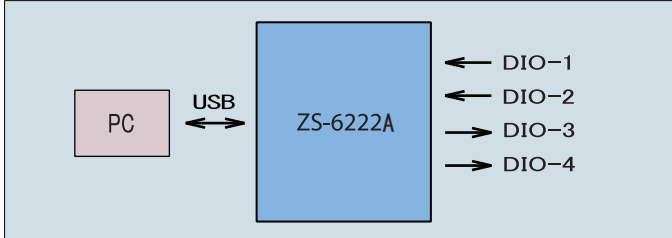
This is example for usage, 16-bit A/D converter input, 8-bit D/A converter output and 8-bit status display LED.



I/O functions

The ZS-6222A can support digital input/output signals up to 4 bytes.
Selection of input and output is done in byte units through software settings.

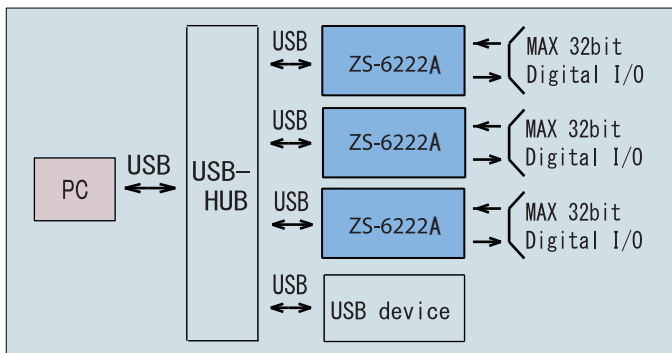
e.g) ZS-6222A is used with 2-byte input and 2-byte output.



Multiple connection is available

ZS-6222A can control multiple ZS-6222A units with a single PC by switching the COM port.

The ZS-6222A is controlled by a virtual COM port.



Control signal

The control signals shown in the table below are prepared so that the USB adapter can synchronize with the connected device.

Name	Signal		Description
	Direction	Type	
STB	OUT	P	It outputs a pulse signal after outputting the data received from the USB to output port.
TRG	OUT	P	It outputs pulse signal by T command.
CLR	OUT	P	It outputs pulse signal by C command.
LAH	IN	P	Data input is latched with the negative pulse of this signal when the latch circuit is enabled. Minimum pulse width 500 μ s.

Note) P of the output signal is available to be set pulse width with P command.

Command

The ZS-6222A considers the first byte sent from the PC as a command and processes it with the character string that follows.
Data is transferred in 2-digit units in HEX code.
The data is followed by a CR+LF code at the end.

Command	Description
W	Data output.
R	Data input. This command sends input byte data
T	Pulse output with TRG signal. Pulse width is set by P command.
C	Pulse output with CLR signal. Pulse width is set by P command.
D	I/O setting with byte unit. Output with "O" of OUT, input with "I" of IN is specified and 4 digit character string is output.
P	Set the pulse width of the control signal with one digit of 0 to 2. There are Three type of pulse widths 10 μ s, 100 μ s, 1 ms
L	Latch circuit is set to enable or disable when the data input. (1: Enable 0: Disable)

※ In addition to the above, there are other commands such as port positive/negative logic setting.

Specifications

Compliant with USB standard 2.0

Amount of data: 32bit (4byte)

I/O can be set in byte unit

I/O level : TTL signal

(Driver IC SN74AC245 or equivalent)

It can be selected Pull Up or Pull Down

Control input: LAH

Control output: STB, TRG, CLR

Connector: 50-core FC connector

(FAP-5001-1202-0BF or equivalent)

Power supply: DC5V 100mA or less

Accessory: Data connector (50-core FC connector)

OS: Windows7 (32/64bit)

Windows8/8.1 (32/64bit)

Windows10 (32/64bit)

Windows11

MacOSX / Linux

*Specifications and appearance are subject to change without notice due to product improvement.



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For more information

