

Based on our experience with GP-IB and RS-232C adapters
 We have developed an even easier-to-use LAN to DIO adapter.
 The PC becomes an FA controller without interface expansion.

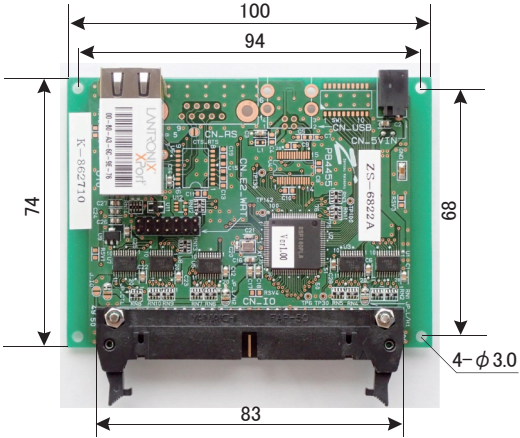
LAN Adapter

ZS-6822AP / AS

Compliant with RoHS

A compact, low-cost, easy-to-handle LAN - DIO adapter that enables digital signals to communicate with a LAN interface. Digital signals such as BCD and binary signals can be easily controlled by importing them to a PC or PLC, and ON/OFF output can be performed from a PC or PLC.

ZS-6822AP



Printed-circuit board type is convenient for built-in and prototype experiments.

74x100x20 (H) mm
Approx. 70g

ZS-6822AS



ZS-6822AP is built into a small case.

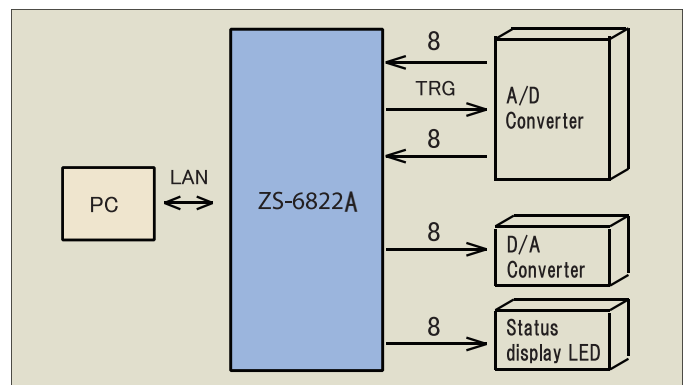
80x110x30 (H) mm
Approx. 180g or less

Features

- Supports Ethernet TCP/IP protocol
- Digital I/O 32 bits (4 bytes)
(Input and output selectable in byte units)
- DIO interface uses 74AC245 with ample room for output drives
- Highly versatile, operating mode is set by command
- Small size
- Low price
- Operationally compatible with the former ZS-6822P/S

Example for usage

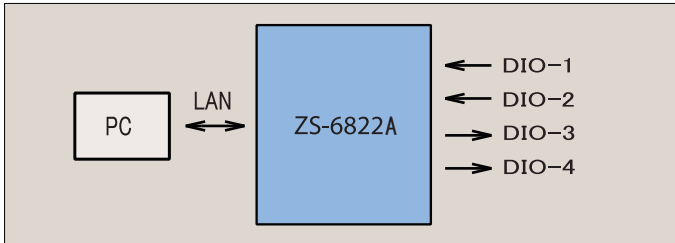
*This is an example for usage as below.
 Input 16-bit A/D converter, output 8-bit D/A converter, and output 8-bit LED status display.*



Both I/O can be selected

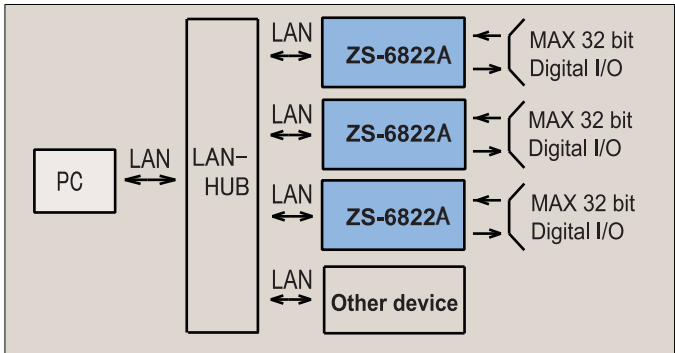
The ZS-6822A can support digital input/output signals up to 4 bytes. Selection of input and output is done in byte units through software settings.

e.g) ZS-6822A is used with 2-byte input and 2-byte output.



Usage for multipul connection

Multiple ZS-6822A units can be controlled by a single PC by setting addresses for each ZS-6822A.



Control signal

The control signals shown in the table below are prepared that the LAN adapter can synchronize with the connected device.

Signal name	Signal		Description
	Direction	Type	
STB	OUT	P	It receives data from the LAN, outputs data to the output port, and then outputs a pulse signal.
TRG	OUT	P	The pulse signal is output by "T" command.
CLR	OUT	P	The pulse signal is output by "C" command.
LAH	IN	P	When the latch circuit is enabled, latch the data input with the negative pulse of this signal. Minimum pulse width 500µs.

Note) P of the OUT signal can select the pulse width with the "P" command.

Command

The ZS-6822A considers the first byte sent from the PC as a command and processes it with the character string that follows. Data is transferred in 2-digit units in HEX code. The data is followed by a CR+LF code at the end.

Command	description
W	Data output.
R	Data input.
T	Pulse output by TRG signal, Pulse width is set with "P" command.
C	Pulse output by CLR signal, Pulse width is set with "P" command.
D	Set I/O in byte units and output 4 digit character string. For I/O setting, designate output with "O" of OUT or specify input with "I" of IN.
P	Set the pulse width of the control signal with one digit of 0 to 2. There are three types of pulse width: 10µs, 100µs, 1ms.
L	Set validity and invalidity of latch circuit when data input. 1: valid, 0: invalid

※ In addition to the above, there are other commands such as port positive/negative logic setting.

Specifications

*Ethernet: Compliant with IEEE802.3
 10/100Base-T Automatic switching
 Protocol TPC/IP, Telnet, ARP, ICMP, HTTP
 Number of Data: 32 bit (4 byte)
 I/O can be set in byte units
 I/O Level: TTL signal(equivalent IC SN74AC245)
 Pull Up or Pull Down can be selected
 Control input: LAH
 Control output: STB, TRG, CLR
 Connector: 50-core FC connector FAP-5001-1202-0F
 Power supply: DC5V 100mA or less
 Accessory: Data connector (50-core FC connector)*

Please download the User's manual from our website.

Opt ion

*AC adapter: GF12-US0520
 Small switching method that does not take space with table taps.
 Input AC 100 to 240V, Output DC 5V 2A*

*Specifications and appearance are subject to change without notice due to product improvement.



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For more information

